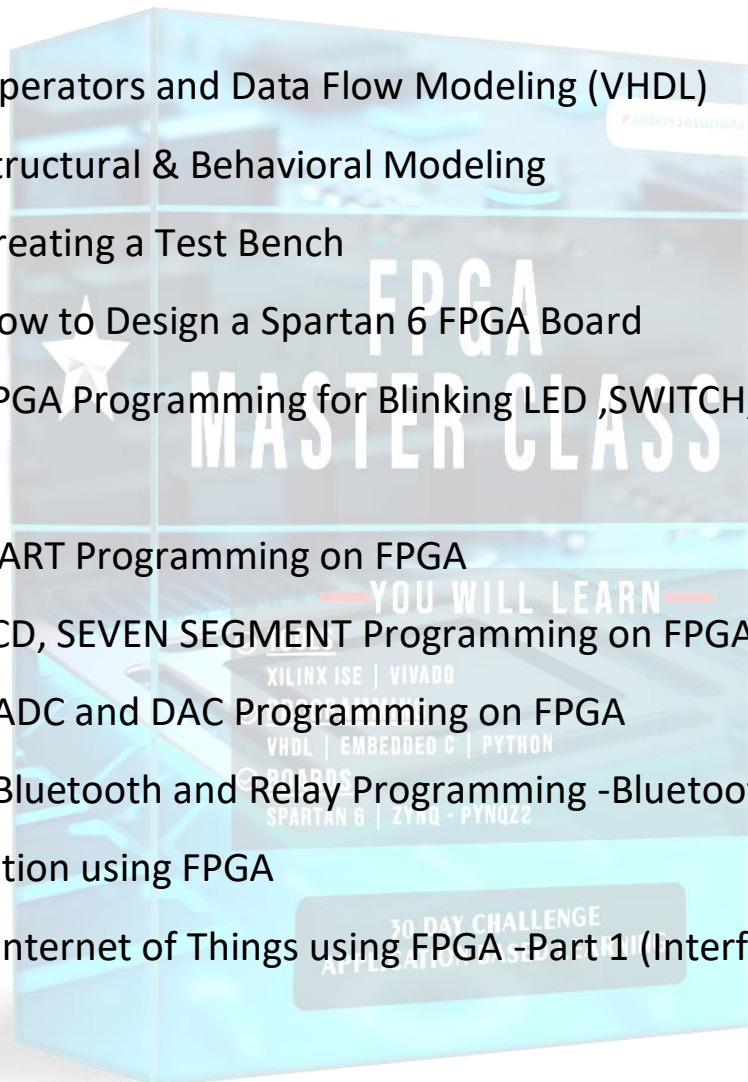


FPGA MASTER CLASS

- + Day 1 Introduction to FPGA
- + Day 2 Introduction to VHDL , How to create a Project in Xilinx ISE .
- + Day 3 Operators and Data Flow Modeling (VHDL)
- + Day 4 Structural & Behavioral Modeling
- + Day 5 Creating a Test Bench
- + Day 6 How to Design a Spartan 6 FPGA Board
- + Day 7 FPGA Programming for Blinking LED ,SWITCH, Relays and Buzzer
- + Day 8 UART Programming on FPGA
- + Day 9 LCD, SEVEN SEGMENT Programming on FPGA
- + Day 10 ADC and DAC Programming on FPGA
- + Day 11 Bluetooth and Relay Programming -Bluetooth Home Automation using FPGA
- + Day 12 Internet of Things using FPGA -Part 1(Interfacing with WIFI)
- + Day 13 Internet of Things using FPGA -Part 2 (Sending Temperature data to Cloud)
- + Day 14 Motor control using FPGA (PWM)
- + Day 15 Embedded system Design using FPGA (C Based programming on FPGA)



WARRIORS WAY COACHING PROGRAM

- + Day 16 Median Filter on Spartan 6 FPGA
 - + Day 17 Edge Detection on FPGA using C Language
 - + Day 18 IoT Programming on FPGA using C Language
 - + Day 19 Debugging with Chip scope PRO
 - + Day 20 Introduction to Python Programming on FPGA
 - + Day 21 Yolo object detection on FPGA
 - + Day 22 Real time edge detection using ZYNQ FPGA (pynq)
 - + Day 23 Real time Moving object detection using ZYNQ FPGA (pynq)
 - + Day 24 Discrete Wavelet Transform using Spartan 6 FPGA(C Language)
 - + Day 25 Image Segmentation using Spartan6 FPGA(Xilinx XPS)
 - + Day 26 Introduction to Vivado Design Suite
 - + Day 27 Implementing LED, UART Using - Vivado Design Suite
 - + Day 28 Machine Learning with Python in PYNQ
 - + Day 29 OpenCV for Image Processing & Video_processing (PYNQ with Python)-ZYNQ
- FPGA**
- + Day 30 Conclusion and Future of VLSI

